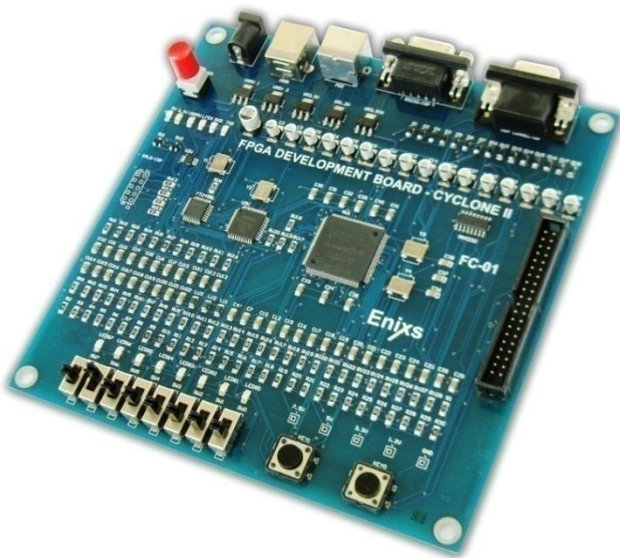


Introduction

The FC-01 FPGA Development board provides a hardware platform for learning high level digital logic operations. It has equipped with Altera cyclone II family chip. This is integrated with 8256 logic elements with 165,888 RAM bits. This board has multiple on-board peripherals as per advanced educational institution requirements. The board has developed with a Universal GPIO connector for interface external I/O cards.



Board Specification

1. FPGA

Cyclone II - EP2C8T144C8N

- LE's - 8256
- PLL - 2
- Embedded Memory Blocks - 36 M4K
- Total Pins - 144
- User I/O - 70
- Package - TQFP

2. In-built USB Blaster

- USB programmer for configure the FPGA
- Made by ALTERA CPLD - EPM3064ATC44-10N

3. On Board Clock

- 6 MHz - FTDI
- 24 MHz - CPLD
- 50 MHz - User clock
- 24 MHz - User clock

4. User Peripherals

- Eight Slide Switches
- Eight LEDs
- Two Push Buttons

5. On-board connectors

- Serial Port
- VGA port
- PS/2 Port
- 40 Pin GPIO expansion connector

Compatible Interfaces

1. ADC 0809 Interface card.
2. DAC 0800 Interface card.
3. DIO-IV Interface card.
4. Elevator Interface card.
5. I2C interface card.
6. Model Train controller Interface card.
7. Motor Interface card.
8. Sensor Interface card.
9. Traffic light Controller interface card.
10. Wireless Interface Card.

